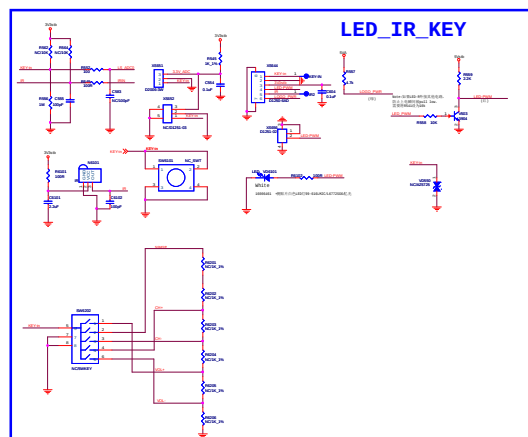
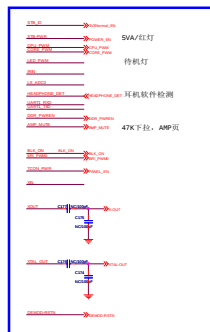
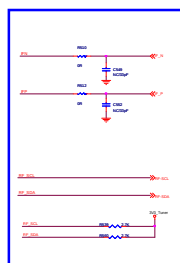
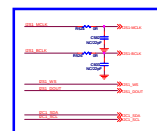
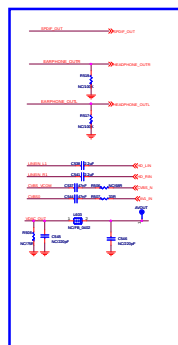
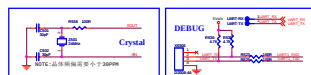
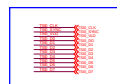
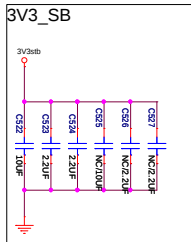
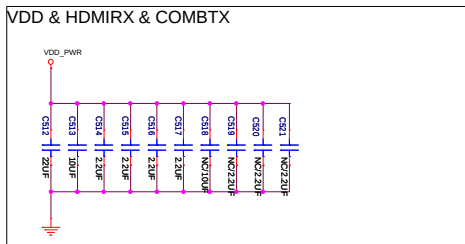
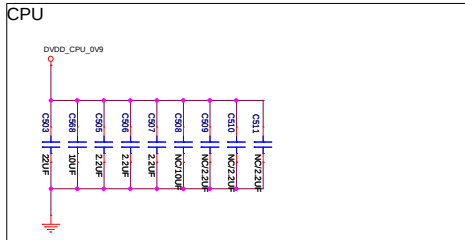
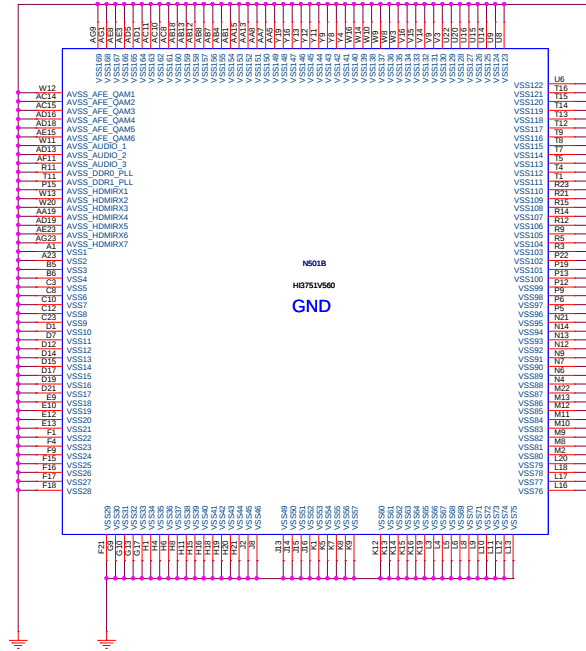
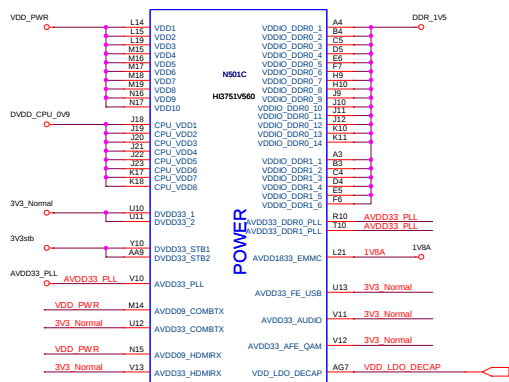
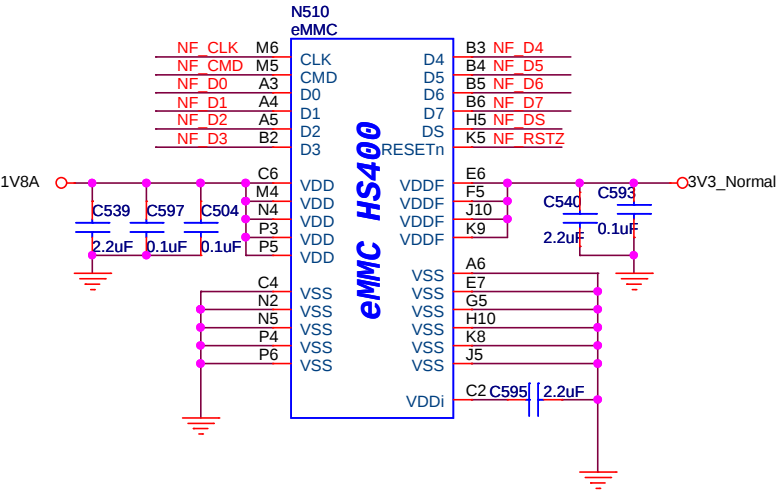


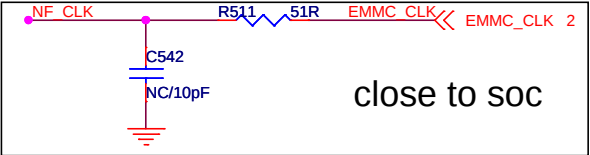
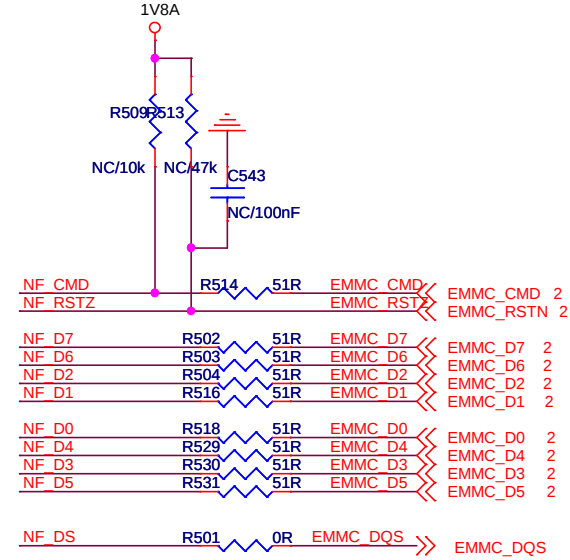
					日期		02. V810
					审核		
44.17	45.85	46.15	47.55	48.47	49.89		02. V810



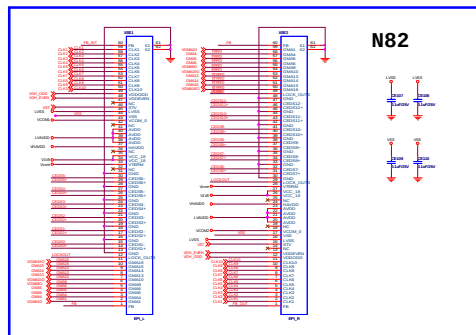
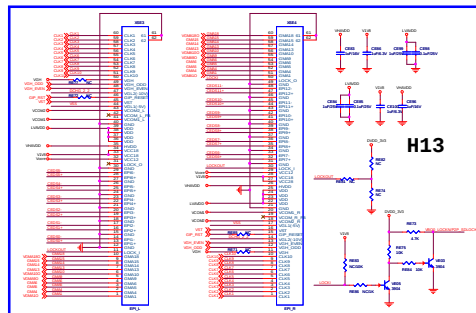
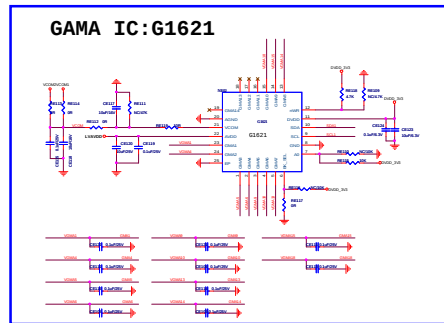
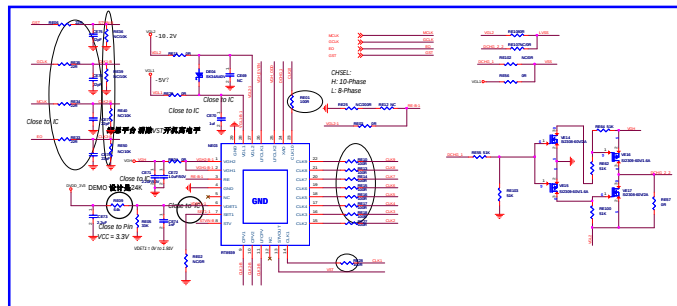
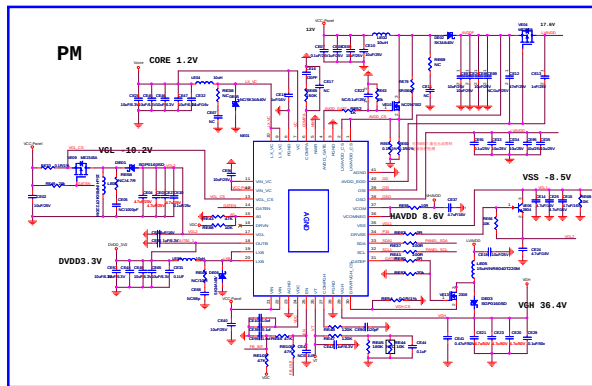
EMMC HS400



The resistance RM15 is placed for Samsung
(KLMAG1JETD-B041 KLMG2JETD-B041 KLMCG4JETD-B041)
EMMC init_error problem.



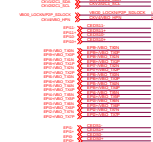
					拟制		06. EMMC HS400
					审核		
标记	数量	更改单号	签名	日期			第 6 张



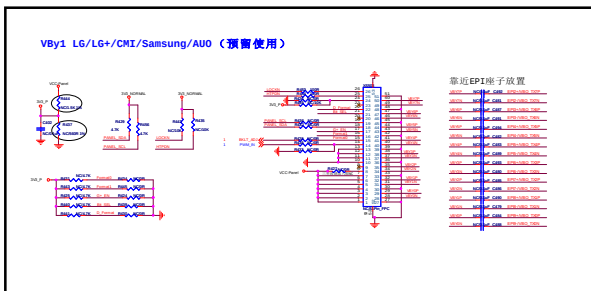
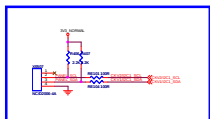
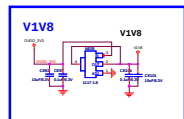
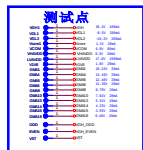
接口名称	EP1	CE05
VGH	RE107	NC
GP1_RST	RE103	OR
VCOMA16	RE113	OR
LOGOUT	RE115	OR
LOGOUT	RE116	OR
VCOMR16	RE105	OR
GP1_RST	RE112	OR
VST	RE103	OR
GP1_RST	RE103	OR
VCOM1	VCOM1/VCOM2	EP1 为开管, CE05 为开管, 连接 17 开孔距电气
VCOM2	VCOM2	EP1 为开管, CE05 为开管, 连接 17 开孔距电气

电气参数	EP1	CE05
VGH	20V	36.4V 100mA
VGL	-10V	-10.2V 100mA
VSS	0V	-0.5V 100mA
VCOM	1.2V	1.2V 100mA
VCOM	1.2V	1.2V 100mA
HAHDD	0.6V	0.6V 100mA
VSS	0V	0V 100mA
VCOM	1.2V	1.2V 100mA

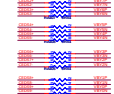
连接SOC 接口



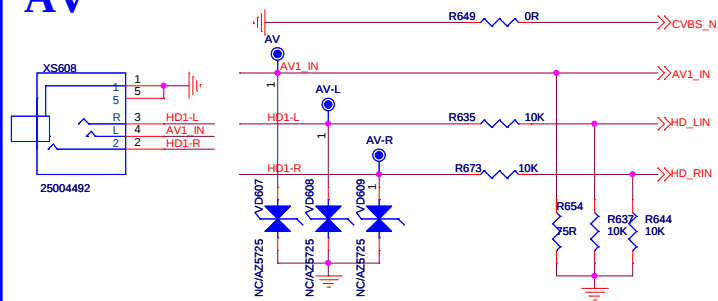
接口名称	EP1	CE05
VGH	RE107	NC
GP1_RST	RE103	OR
VCOMA16	RE113	OR
LOGOUT	RE115	OR
LOGOUT	RE116	OR
VCOMR16	RE105	OR
GP1_RST	RE112	OR
VST	RE103	OR
GP1_RST	RE103	OR
VCOM1	VCOM1/VCOM2	EP1 为开管, CE05 为开管, 连接 17 开孔距电气
VCOM2	VCOM2	EP1 为开管, CE05 为开管, 连接 17 开孔距电气



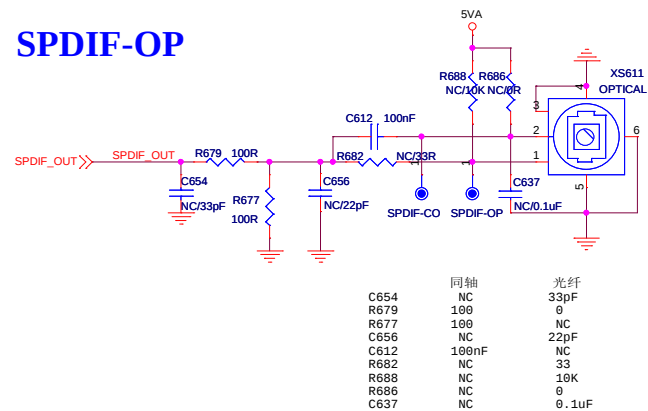
靠近EP1端子位置, 靠近VSS端子



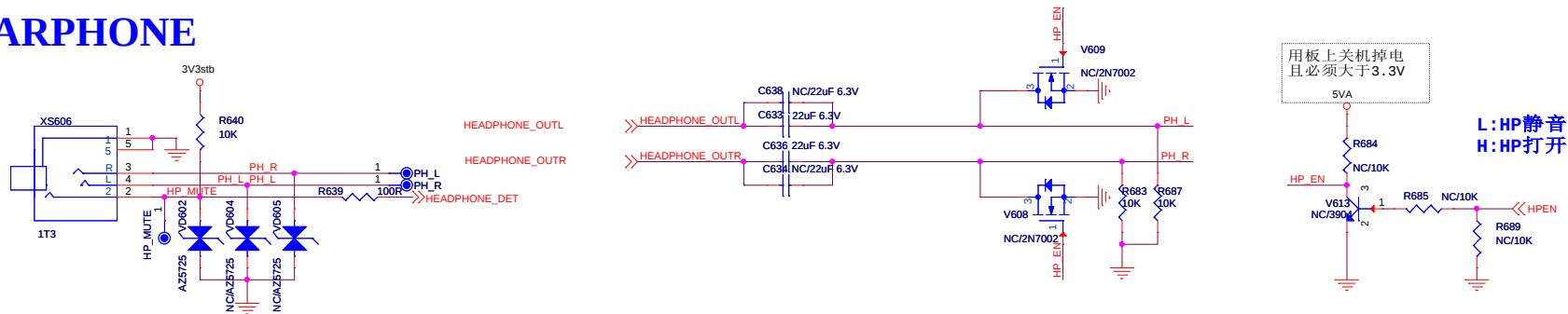
AV



SPDIF-OP

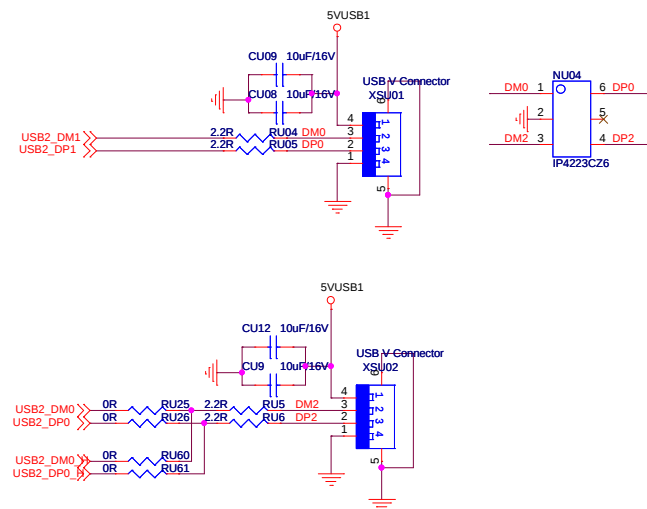


EARPHONE

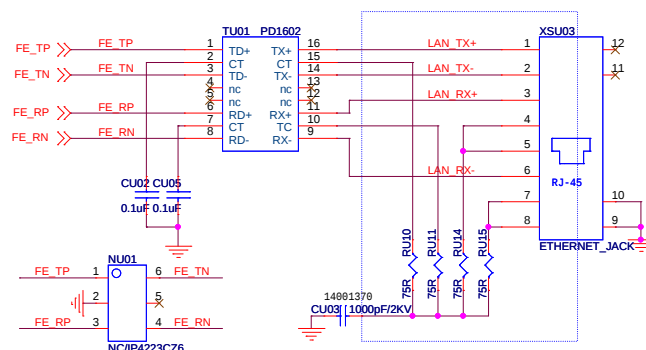


					拟制		08. Video
					审核		
标记	数量	更改单号	签名	日期			第 8 张

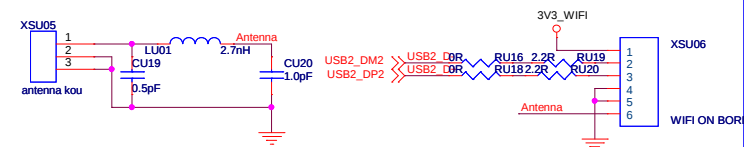
USB



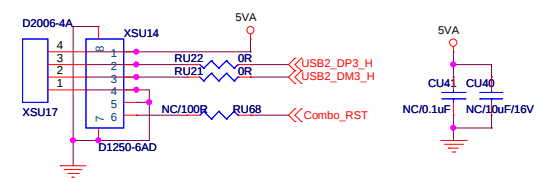
Ethernet



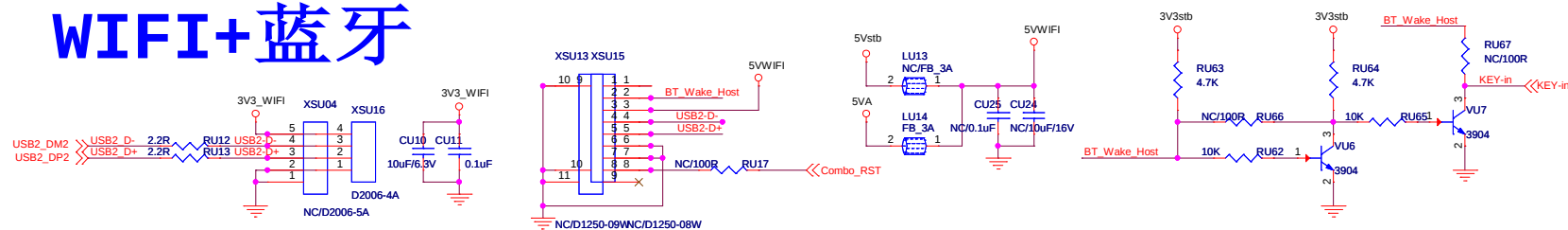
WIFI ON BORD



WIFI

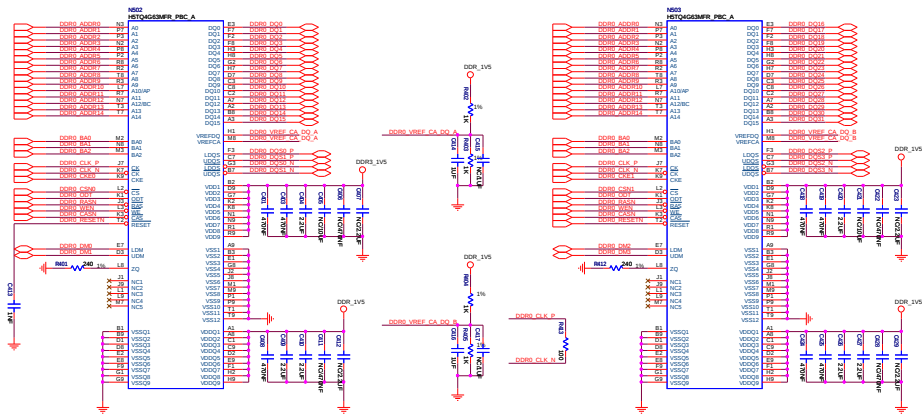


WIFI+蓝牙



					拟制	09. USB&Ethernet
					审核	
标记	数量	更改单号	签名	日期		第 9 张

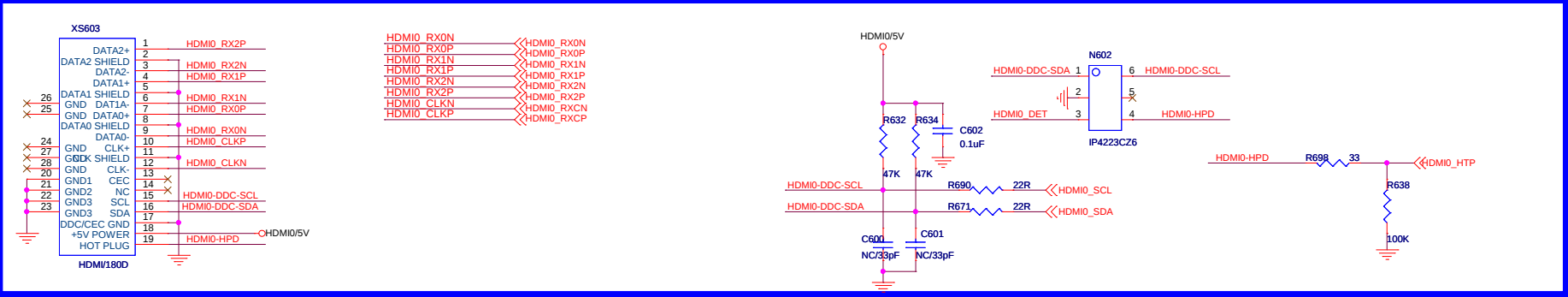
9.DDRPHY0_DDR3_2L_32BIT



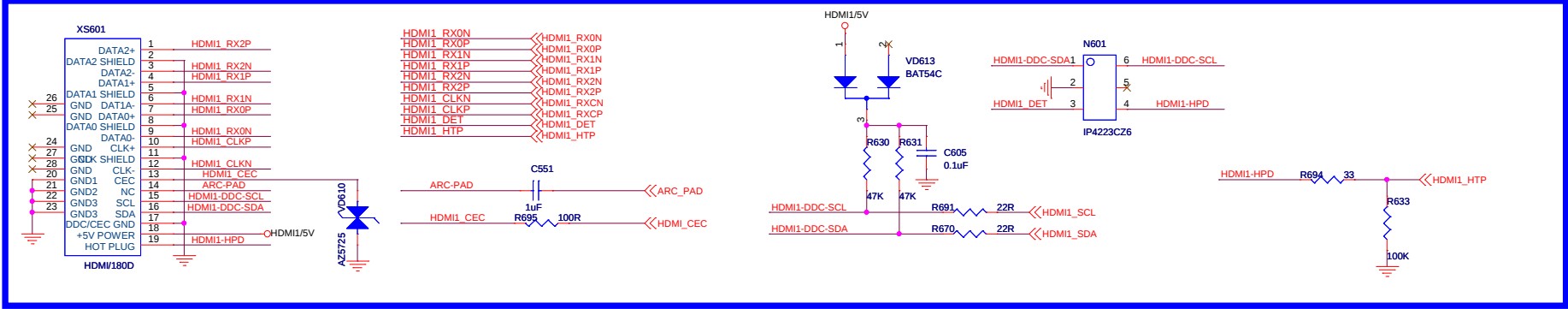
The type and specification of the components refer to the BOM

				NA	
				ECA NO	DATE
DESIGNED	ZHANGSAN XXXXX		H07S1V500DMD00_VA		00001234
REVIEWED	LSI XXXXX				
		VER	PART NUMBER		SHEET 9 of 28
		A	00030001		HSilicon TECH CO.,LTD.

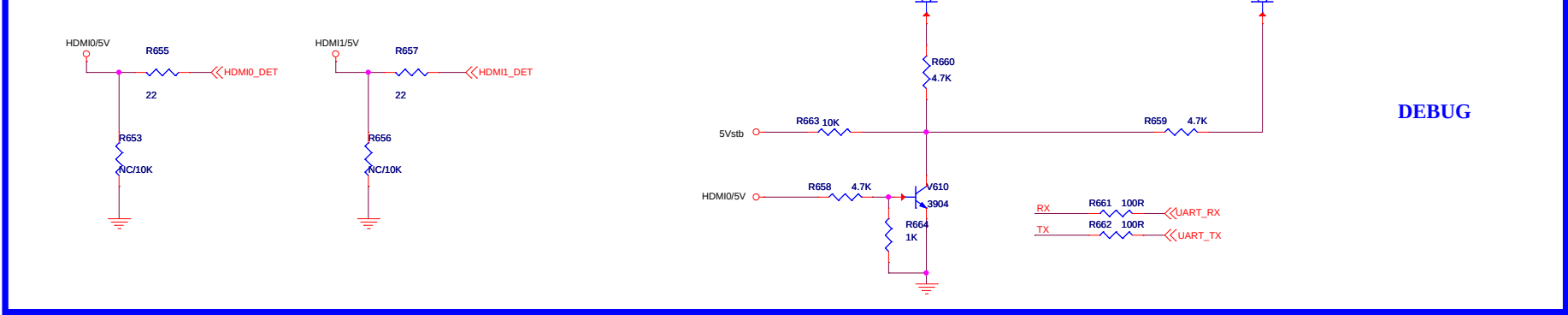
HDMI0 Debug



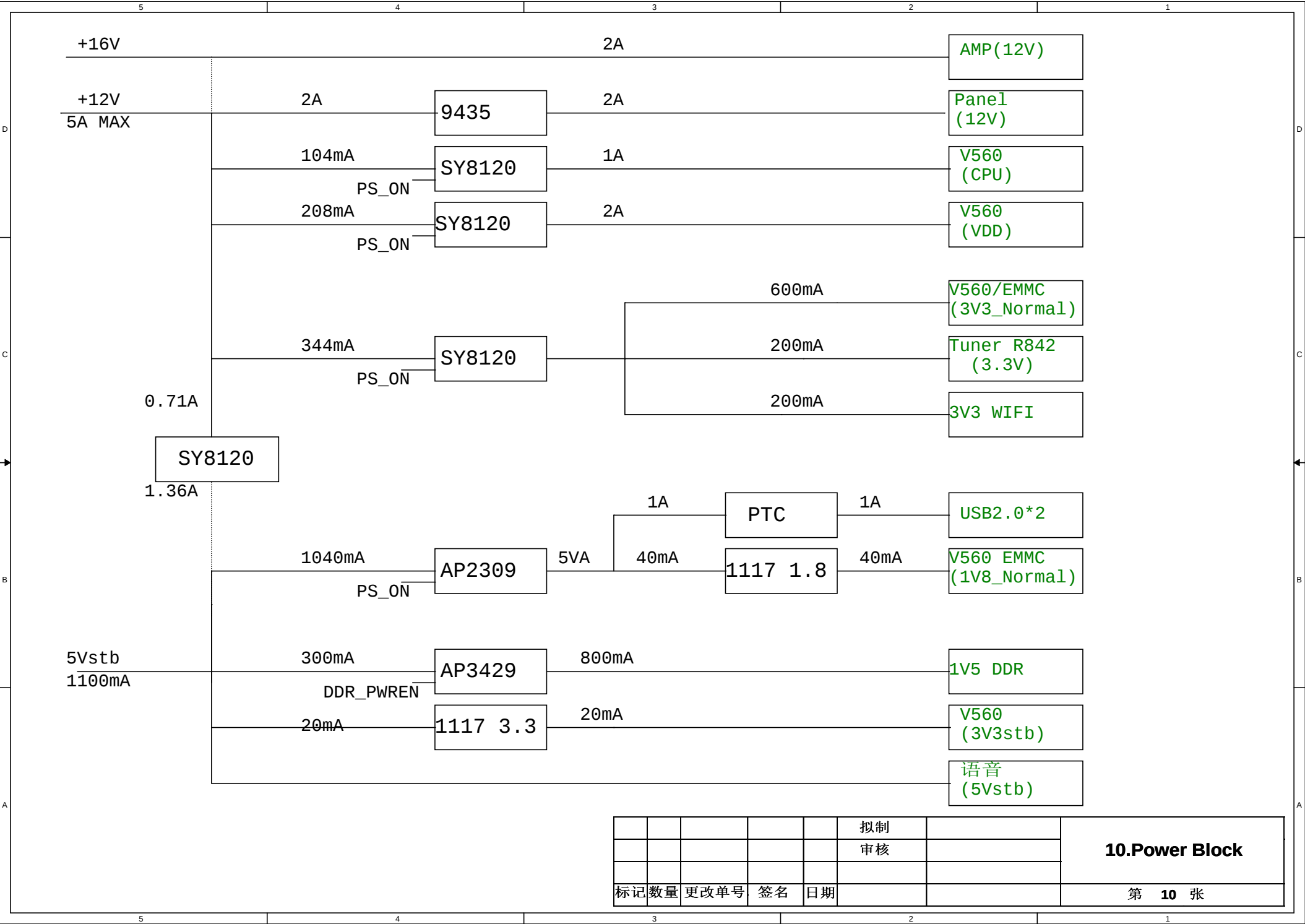
HDMI1 CEC ARC



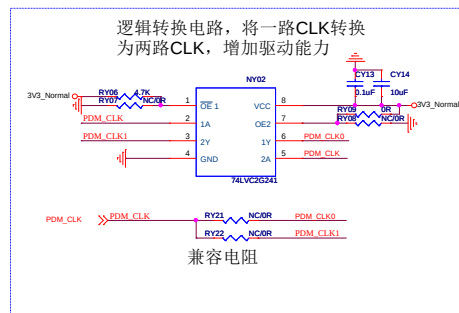
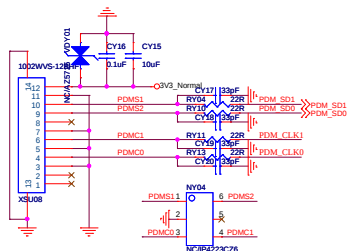
HDMI DETECT



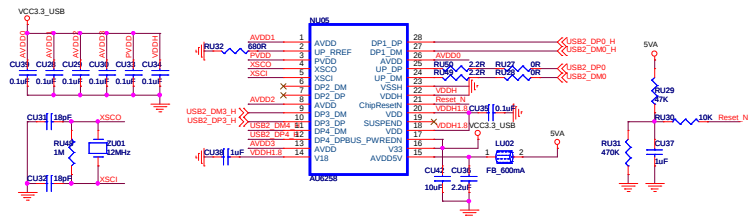
					拟制		10. HDMI
					审核		
标记数量	更改单号	签名	日期				第 10 张



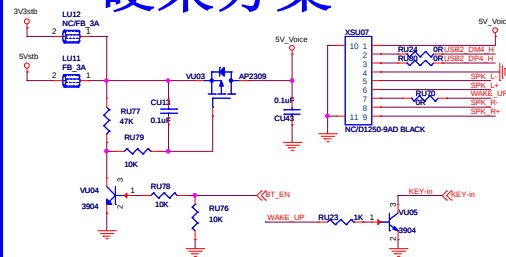
直连MIC小板



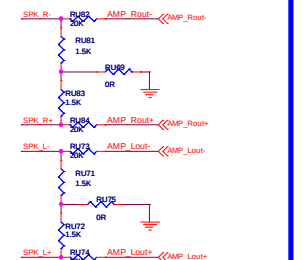
USB HUB



硬采方案



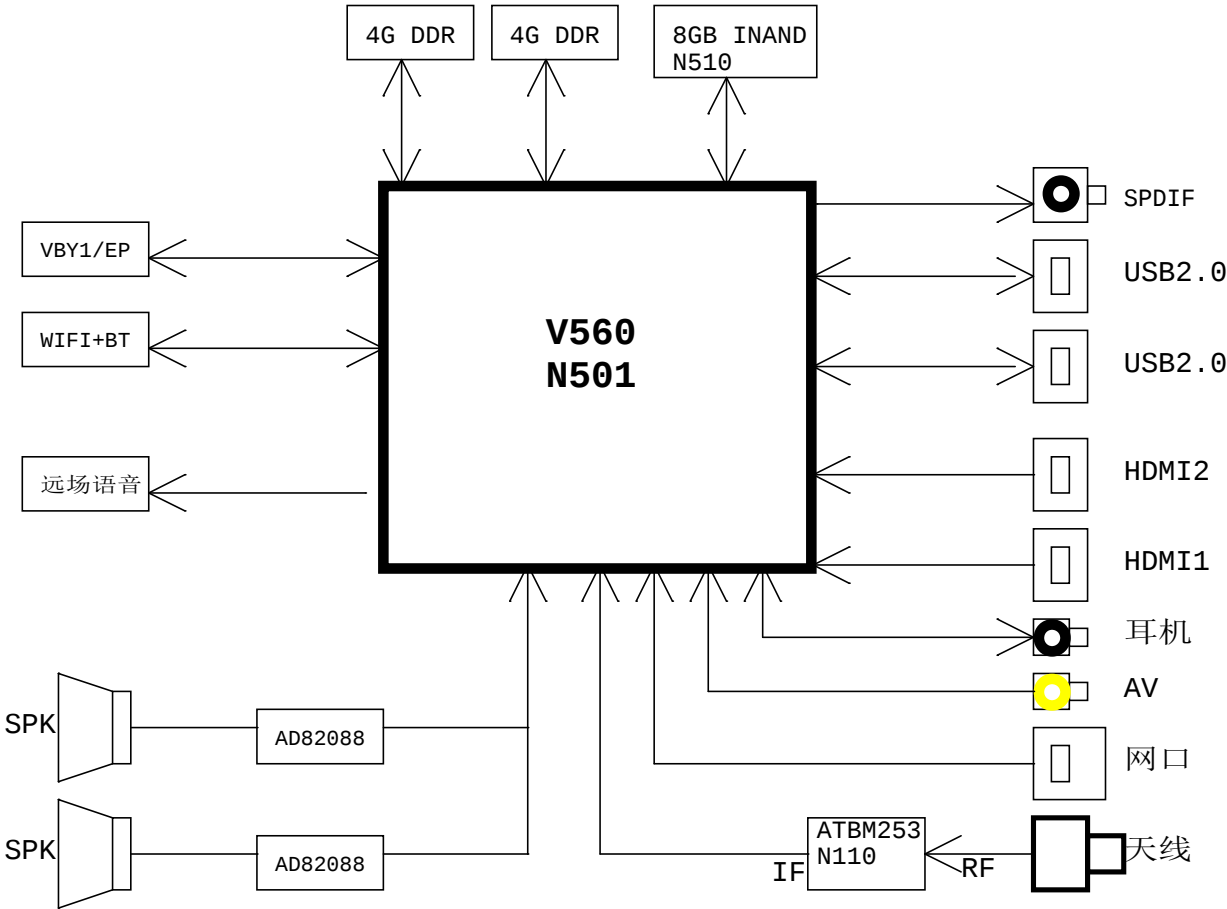
分压电路



注意：必须确保SPK+/-的信号 $\leq 1V_{rms}$ ；根据实际设计调整电阻阻值

					拟制		13. VOICE
					审核		
标记	数量	更改单号	签名	日期			第 13 张

V810原理框图



					拟制		13.系统框图
					审核		
标记	数量	更改单号	签名	日期			第 13 张

date	change list
2019.08.10	首版PCB

					拟制		14.修改记录
					审核		
标记	数量	更改单号	签名	日期			第 14 张